

INFORMATION DISCLOSURE CITATION

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Applicant(s)

Claude R. GAUTHIER, et al.

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2836

*EXAMINER
INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

RF

B1

Dennis J. Herrell, et al.; "Modeling of Power Distribution Systems for High-Performance Microprocessors"; IEEE Transactions on Advanced Packaging; Vol. 22, No. 3, August 1999, pp. 240-248

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B2

Wiren D. Becker, et al.; "Modeling, Simulation, and Measurement of Mid-Frequency Simultaneous Switching Noise in Computer Systems"; IEEE Transactions on Components, Packaging, and Manufacturing Technology - Part B; Vol. 21, No. 2, May 1998; pp. 157-163

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B3

Mondira Deb Pant, et al.; "On-Chip Decoupling Capacitor Optimization Using Architectural Level Current Signature Prediction"; 13th Annual IEEE International ASIC/SOC Conference; September 13-16, 2000; pp. 288-292

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